



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Eigo NAKAGAWA et al.

Application No.: 10/721,461

Docket No.: 117863

Filed: November 26, 2003

For: CIRCUIT BOARD INSPECTION DEVICE

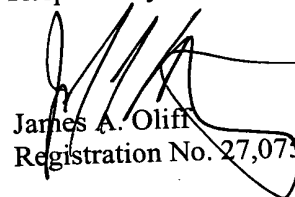
REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,



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Date: September 30, 2004

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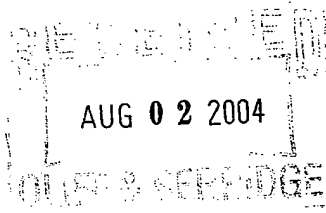
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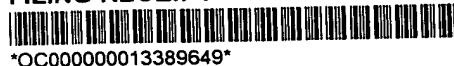

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/721,461	11/26/2003	2858	1324	117863	18/19	46	4

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CONFIRMATION NO. 2767

FILING RECEIPT



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Date Mailed: 07/30/2004

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Assignment For Published Patent Application

FUJI XEROX Co., LTD, Minato-ku, JAPAN;

Domestic Priority data as claimed by applicant

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If Required, Foreign Filing License Granted: 07/28/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Circuit board inspection device

Preliminary Class

324

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